

## 2764 to TMS2564 Adapter

Here's the rest of the pins. I double checked against the JEDEC standard.

2764 follows JESD 21C standard for an 8K EPROM or RAM.

### 2764

### TMS2564

<b>PIN#2</b>	<b>PIN#23</b>	<b>A12</b>
<b>PIN#20</b>	<b>PIN#2</b>	<b>Chip Enable (CE)*</b>
<b>PIN#22</b>	<b>PIN#27</b>	<b>Output Enable (OE)*</b>
<b>PIN#23</b>	<b>PIN#20</b>	<b>A11</b>
<b>PIN#27</b>	<b>PIN#22</b>	<b>PGM*</b>

**CE\* and OE\* usually differ only in timing requirements. A chip needs both low for reading. This is not a problem with 4/A timing.**